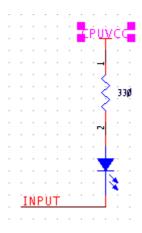
MICRO1 POWER HEADER

	. 🗸 2	 1 🔻	·
_+12V	^ 4	 3	+12V
GND	6	 5	GND
GND	8	7	GND
GND	1Ø	9	GND
+5V	12	 11	+5V
+5V	14	 13	+5V

PULSE1	1		2	DIR1		
PWR1	3		4	PULSE2		
DIR2	5		6	PWR2		
PULSE3	7		8	DIR3		
PWR3	9		10	PULSE4		
DIR4	11		12	PWR4		
PULSE5	13		14	DIR5		
PWR5	15		16	PULSE6		
DIR6	17		18	PWR6		
PULSE7	19		20	DIR7		
PWR7	21		22	PULSE8		
DIR8	23		24	CPUVCC		
PWR8	25		26	CPUGND		
HEADER 13X2						



INPUT should be pulled to CPUGND to provide low signal to drive. This configuration is typical to PULSE, PWR, and DIR signals.